

VIGNAN'S LARA INSTITUTE OF TECHNOLOGY & SCIENCE :: VADLAMUDI EXAMINATION CELL

VLITS/ EXAM CELL/ EXTERNAL TIME TABLE/ 2015/ 04

TIME TABLE

Course & Sem: II B.Tech I Semester R13

Date: 19.11.2015

Academic Year: 2015 - 16

Timings: 02.00 To 05.00 pm

Branch	Date & Day					
	01-12-2015 (Tuesday)	03-12-2015 (Thursday)	05-12-2015 (Saturday)	08-12-2015 (Tuesday)	10-12-2015 (Thursday)	12-12-2015 (Saturday)
CE	Fluid Mechanics	Basic Electrical and Electronics Engineering	Probability & Statistics	Strength of Materials - I	Surveying	Building Materials and Construction
EEE	Electrical Circuit Analysis - II	Thermal and Hydro Prime Movers	Basic Electronics and Devices	Complex Variables and Statistical Methods	Electro Magnetic Fields	Electrical Machines-I
ME	Metallurgy & Materials Science	Basic Electrical and Electronics Engineering	Thermodynamic	Managerial Economics and Financial Analysis	Mechanics of Solids	---
ECE	Data structures	Electrical Technology	Electronic Devices and Circuits	Managerial Economics and Financial Analysis	Signals & Systems	Environmental Studies
CSE	Data Structures	Mathematical Foundations of Computer Science	Object Oriented Programming Through C ++	Managerial Economics and Financial Analysis	Digital logic Design	---
IT	Data Structures	Mathematical Foundations of Computer Science	Object oriented Programming Through C ++	Managerial Economics and Financial Analysis	Digital logic Design	---

M. Ratna Doley
I/c Exam Cell



(Signature)
Principal

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