

VIGNAN'S LARA INSTITUTE OF TECHNOLOGY & SCIENCE :: VADLAMUDI EXAMINATION CELL

VLITS/ EXAM CELL/ EXTERNAL TIME TABLE/ 2015/ 02

TIME TABLE

Course & Sem: III B.Tech I Semester R13

Date: 29.10.2015

Academic Year: 2015 - 16

Timings: 10.00 am To 01.00 pm

BRANCHES	Date & Day						
	09.11.2015 (Monday)	12.11.2015 (Thursday)	14.11.2015 (Saturday)	16.11.2015 (Monday)	18.11.2015 (Wednesday)	20.11.2015 (Friday)	24.11.2015 (Monday)
EEE	Electrical Measurements	Managerial Economics and Financial Analysis	Power Systems - II	Electrical Machines - III	Power Electronics	Linear & Digital IC Applications	IPR & Patents
ME	Dynamics of Machinery	Metal Cutting & Machine Tools	Design of Machine Members-I	Instrumentation & Control Systems	Thermal Engineering - II	Metrology	IPR & Patents
ECE	Pulse & Digital Circuits	Linear IC Applications	Control Systems	Digital System Design & Digital IC Applications	Antennas and Wave Propagation	---	IPR & Patents
CSE	Compiler Design	Data Communication	Principles of Programming Languages	Database Management Systems	Operating Systems	---	---
IT	Software Engineering	Data Communication	Advanced JAVA	Database Management Systems	Operating Systems	---	---

P. Ramesh Babu
I/c Exam Cell



[Signature]
Principal

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*Vignan's Lara Institute of Technology & Science
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