

550

VIGNAN'S LARA INSTITUTE OF TECHNOLOGY & SCIENCE :: VADLAMUDI EXAMINATION CELL

VLITS/ EXAM CELL/ EXTERNAL TIME TABLE/ 2015/ 03

TIME TABLE

Course & Sem: IV B.Tech I Semester R10

Date: 05.11.2015

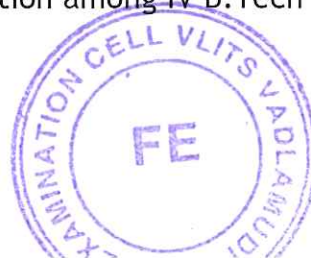
Academic Year: 2015 - 16

Timings: 10.00 am To 01.00 pm

BRANCHES	Date & Day					
	23.11.2015 (Monday)	25.11.2015 (Wednesday)	27.11.2015 (Friday)	30.11.2015 (Monday)	02.12.2015 (Wednesday)	04.12.2015 (Friday)
EEE	Computer Organization	High Voltage Engineering	Switch Gear & Protection	Power System Operation & Control	Electrical Distribution Systems	Instrumentation
ME	Refrigeration & Air Conditioning	CAD/CAM	Alternative Sources of Energy	Unconventional Machining Processes	Automobile Engineering	Instrumentation
ECE	Optical Communications	Embedded Systems	Digital image Processing	Radar Systems	Telecommunication switching Systems	Bio-Medical Engineering
CSE	Cryptography and Network Security	UML & Design Patterns	Data Ware Housing and Data Mining	Mobile Computing	Software Project Management	Open Source Software
IT	Cryptography and Network Security	Design Patterns	Data Ware Housing and Data Mining	Mobile Computing	Artificial Intelligence	Open Source Software

M. Satish Babu
I/c Exam Cell

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(Signature)
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